

SM5391 THRU SM5397

SURFACE MOUNT GLASS PASSIVATED RECTIFIERS

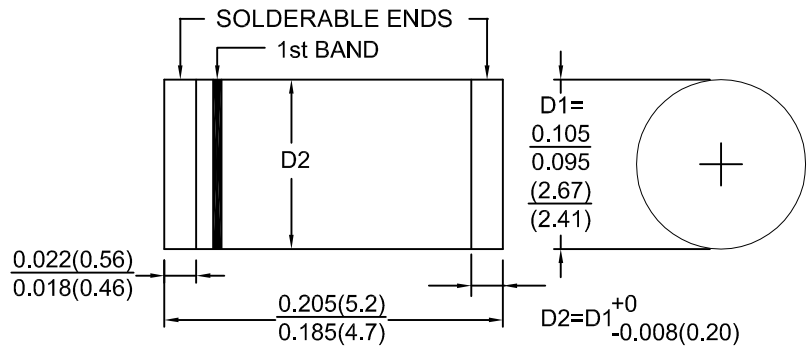
FEATURES:

- Ideal for surface mount applications
- Easy pick and place
- Built-in strain relief
- Glass passivated Chip

MECHANICAL DATA

Case : Molded plastic use UL 94V-0 recognized flame retardant epoxy
 Terminals : Plated terminals, solderable per MIL-STD-202, Method 208 guaranteed
 Polarity : Silver color band on body denotes cathode
 Mounting Position : Any
 Weight : 0.116 grams, 0.0046 ounce

MELF / DO-213AB



1st band denotes type positive and (cathode)

Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25° C ambient temp. unless otherwise specified.
 Single phase, half sine wave, 60 Hz, resistive or inductive load.
 For capacitive load, derate current by 20 %.

Characteristic	Symbol	SM 5391	SM 5392	SM 5393	SM 5394	SM 5395	SM 5396	SM 5397	Units
Maximum recurrent peak reverse voltage	VRRM	50	100	200	400	600	800	1000	Volts
Maximum RMS voltage	VRMS	35	70	140	280	420	560	700	Volts
Maximum DC blocking voltage	VDC	50	100	200	400	600	800	1000	Volts
Maximum average forward rectified current at TL=75° C	I(AV)	1.5							Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load(JEDEC Method)	IFSM	50							Amps
Maximum instantaneous forward voltage drop at 1.5	VF	1.1							Volts
Maximum DC reverse current at rated DC blocking voltage	IR			Ta=25° C		Ta=125° C			μ A
Maximum full load reverse current full cycle average at Ta=75° C	IRO	30.0							μ A
Typical thermal resistance	Rth-JA Rth-JL	75 30							° C/W
Typical junction capacitance	Cj	12							pF
Operating junction temperature range	Tj	-65 to +150							° C
Storage temperature range	Tstg	-65 to +150							° C

RATINGS AND CHARACTERISTIC CURVES SM5391THRU SM5397

FIG.1-DERATING CURVE FOR OUTPUT RECTIFIER CURRENT

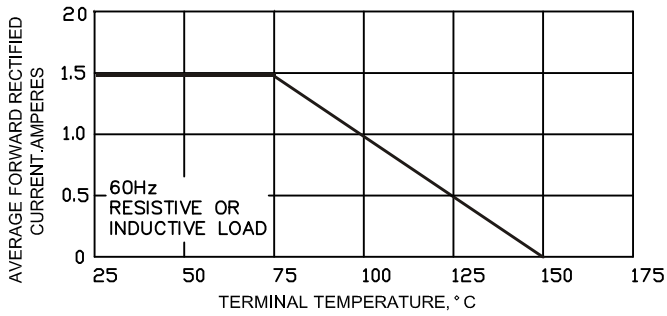


FIG.2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

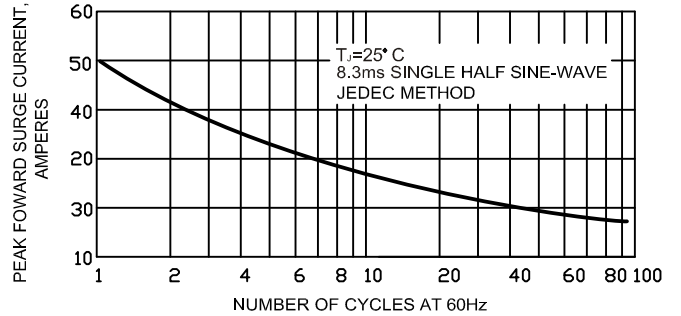


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

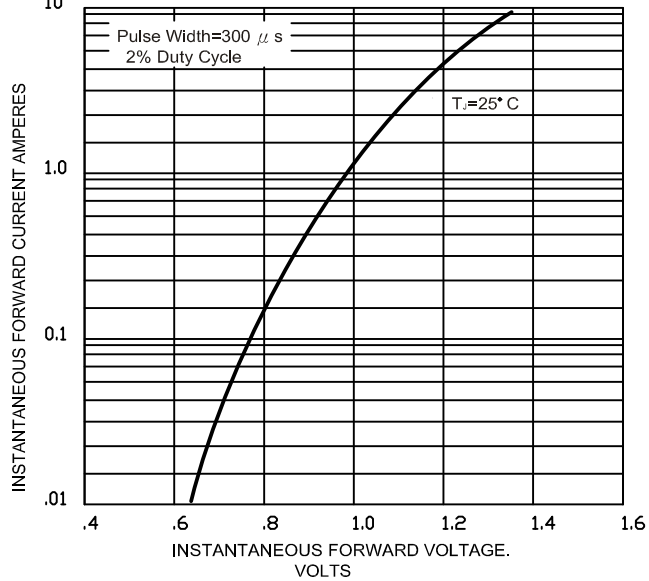


FIG.4-TYPICAL REVERS CHARACTERISTICS

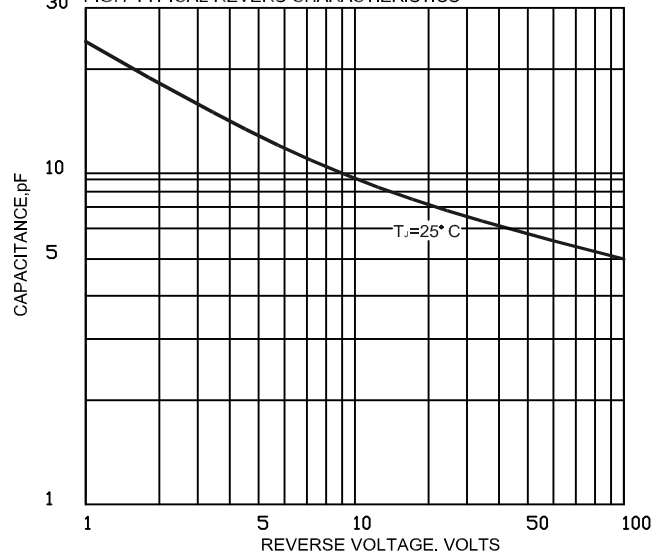


FIG.5-TYPICAL JUNCTION CAPACITANCE

